

Low Noise Silicon Bipolar RF Transistor

- For low noise, high-gain amplifiers up to 2 GHz
- For linear broadband amplifiers
- f_T = 8 GHz, NF_{min} = 1 dB at 900 MHz
- Pb-free (RoHS compliant) package
- Qualification report according to AEC-Q101 available





ESD (Electrostatic discharge) sensitive device, observe handling precaution!

Туре	Marking	Pin Configuration			Package
BFR193	RCs	1 = B	2 = E	3 = C	SOT23

Maximum Ratings at T_A = 25 °C, unless otherwise specified

Parameter	Symbol	Value	Unit
Collector-emitter voltage	$V_{\sf CEO}$	12	V
Collector-emitter voltage	V_{CES}	20	
Collector-base voltage	V_{CBO}	20	
Emitter-base voltage	V_{EBO}	2	
Collector current	I _C	80	mA
Base current	l _B	10	
Total power dissipation ¹⁾	P _{tot}	580	mW
<i>T</i> _S ≤ 69°C			
Junction temperature	T_{J}	150	°C
Storage temperature	T_{Stq}	-55 150	

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ²⁾	R _{thJS}	140	K/W

 $^{{}^1}T_{\rm S}$ is measured on the collector lead at the soldering point to the pcb

 $^{^2}$ For calculation of R_{thJS} please refer to Application Note AN077 (Thermal Resistance Calculation)



Electrical Characteristics at T_A = 25 °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics					
Collector-emitter breakdown voltage	V _{(BR)CEO}	12	-	-	V
$I_{\rm C}$ = 1 mA, $I_{\rm B}$ = 0					
Collector-emitter cutoff current	I _{CES}	-	_	100	μΑ
$V_{CE} = 20 \text{ V}, V_{BE} = 0$					
Collector-base cutoff current	I _{CBO}	-	-	100	nA
$V_{\rm CB} = 10 \text{ V}, I_{\rm E} = 0$					
Emitter-base cutoff current	I _{EBO}	-	_	1	μA
$V_{\rm EB} = 1 \text{ V}, I_{\rm C} = 0$					
DC current gain	h _{FE}	70	100	140	-
$I_{\rm C}$ = 30 mA, $V_{\rm CE}$ = 8 V, pulse measured					



Electrical Characteristics at T_A = 25 °C, unless otherwise specified

g) f _T C _{cb}	6 -	8 0.66	- 1	GHz
f _T	6		- 1	GHz
C _{cb}	-		1	GHz
	-	0.66	1	
	-	0.66	1	+
Coo			1	pF
Coo				
Coo				
_ ce	-	0.28	-	
C _{eb}	-	2.25	-	
NF _{min}				dB
	-	1	-	
	-	1.6	_	
G _{ma}]
	-	15	-	
	-	10	_	
S _{21e} ²				dB
	-	13	_	
	-	7.5	_	
IP ₃	-	30	-	dBm
P _{-1dB}	-	13	-	1
	1	1	1	1
		NF _{min} - - -	NF _{min} - 1 - 1.6 G _{ma} - 15 - 10 S _{21e} ² - 13 - 7.5 IP ₃ - 30	NF _{min} - 1 1.6 - G _{ma} - 15 10 - S _{21e} ² - 13 7.5 - IP ₃ - 30 -

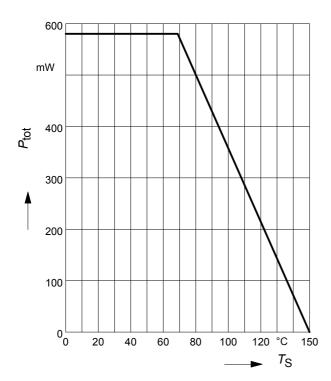
 $^{{}^{1}}G_{\text{ma}} = |S_{21} / S_{12}| (k-(k^{2}-1)^{1/2})$

²IP3 value depends on termination of all intermodulation frequency components.

Termination used for this measurement is 50Ω from 0.2 MHz to 12 GHz

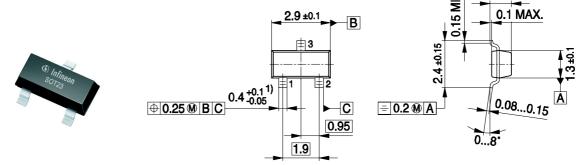


Total power dissipation $P_{\text{tot}} = f(T_{\text{S}})$





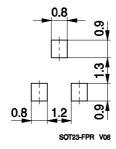
Package Outline



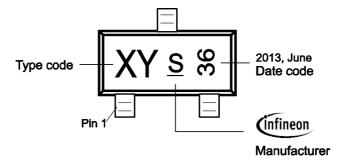
1) Lead width can be 0.6 max. in dambar area

SOT23-PO V08

Foot Print



Marking Layout (Example)



Standard Packing

Reel Ø 180 mm = 3.000 Pieces/ Reel

Reel Ø 330 mm = 10.000 Pieces/ Reel

O.9

O.2

Pin 1

3.15

SOT23-TP V02



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